

TEST " 48E86660

ET420197775US

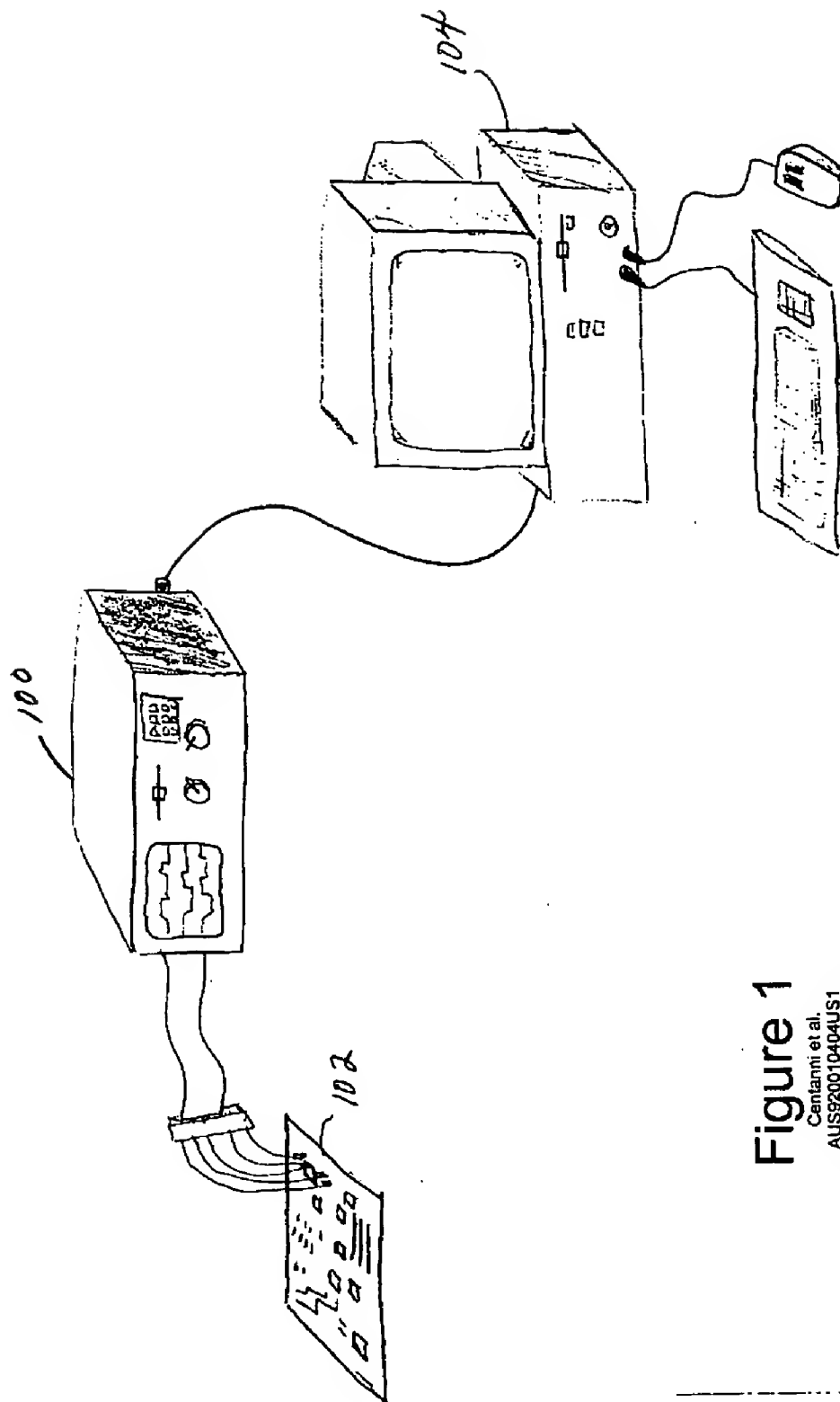
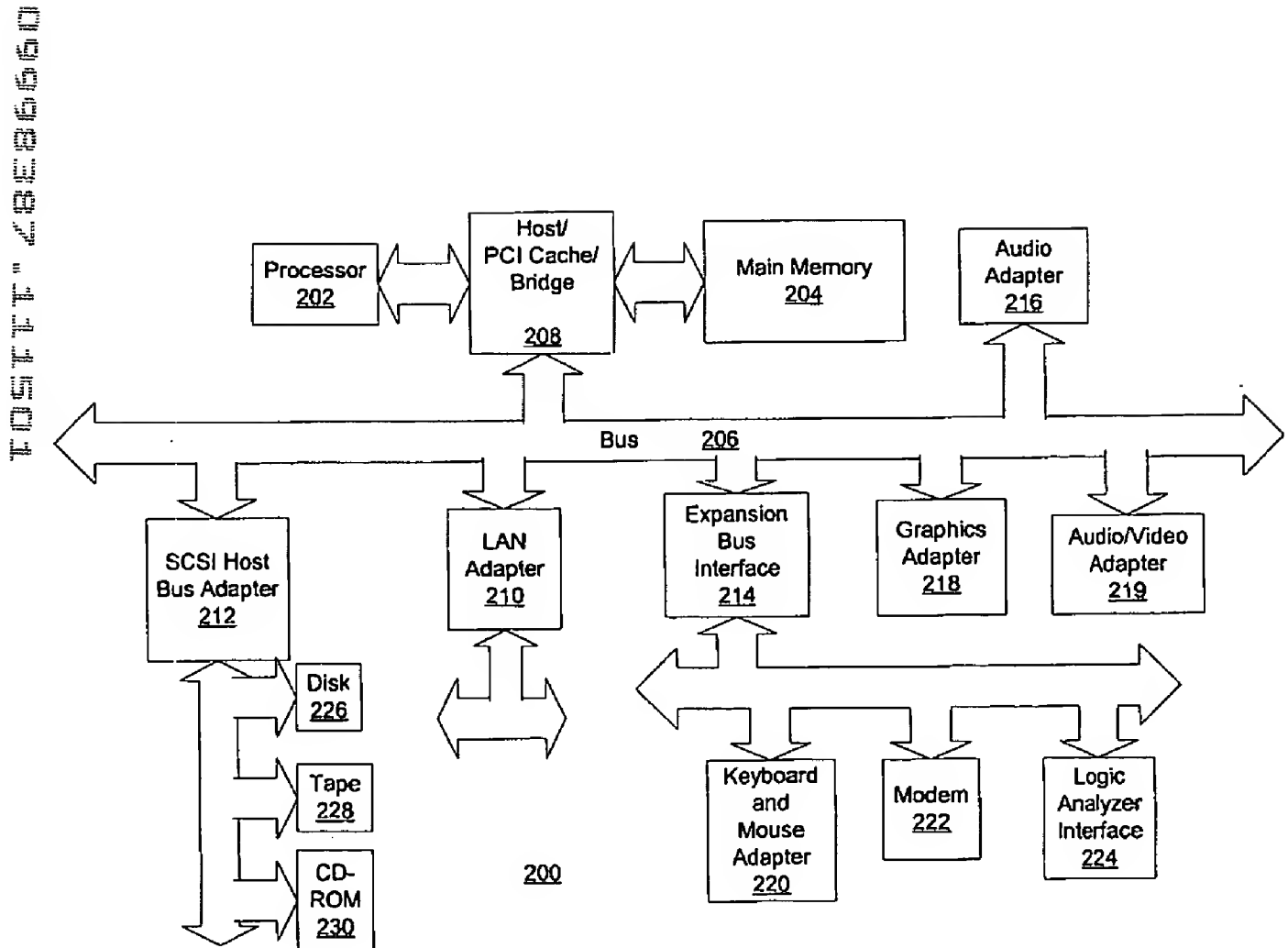


Figure 1

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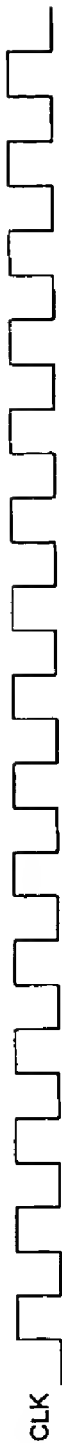
Figure 2

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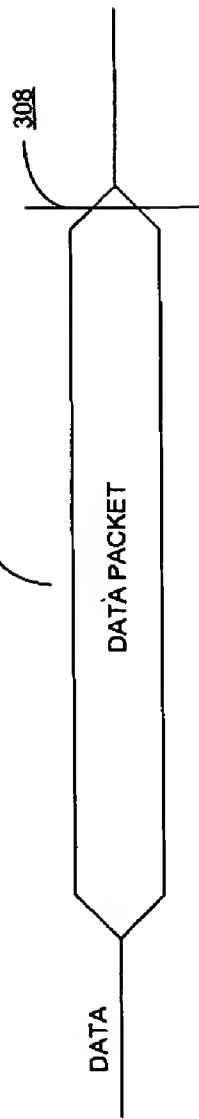


POSTERIOR BUS TRACE ANALYSIS

300

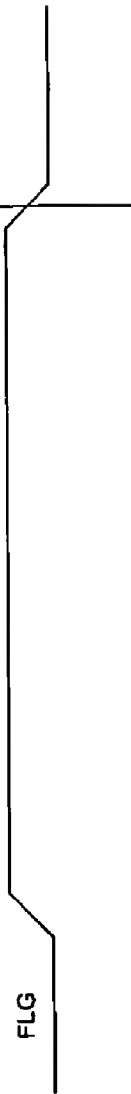


302



308

304



306

Figure 3

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Figure 4A

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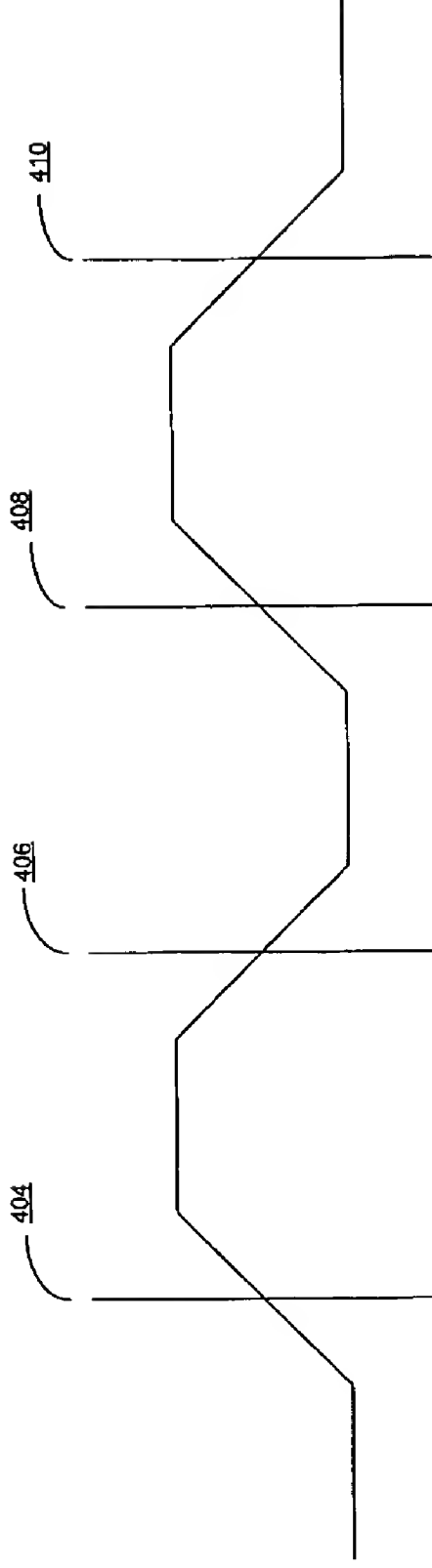
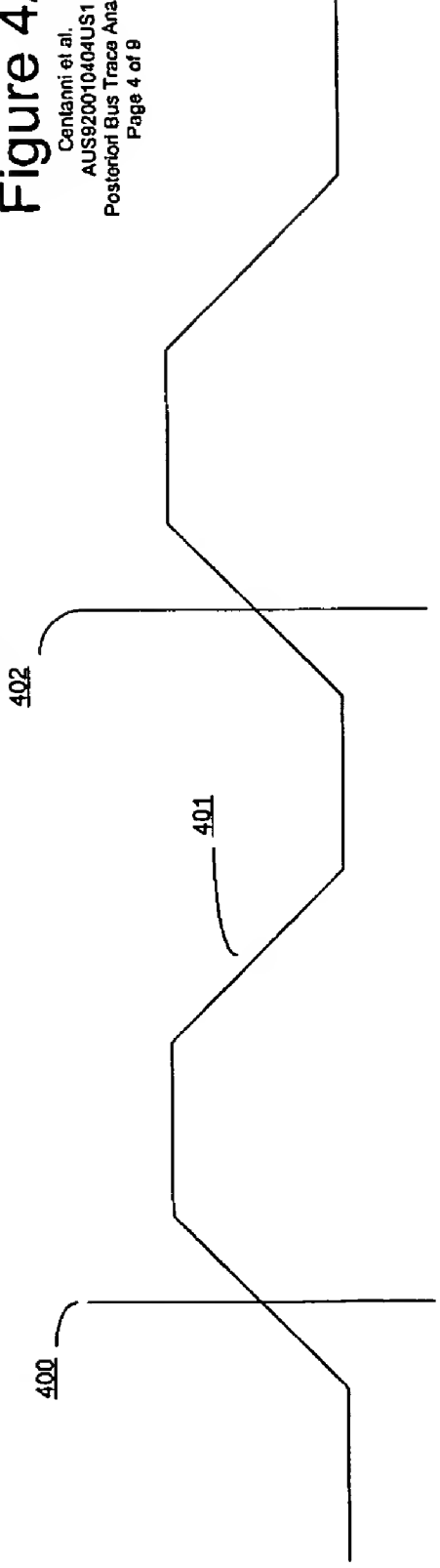


Figure 4B

Figure 1 shows a 10x10 grid of bits. The grid is labeled with 500 on the left, 502, 504, and 506 at the top, and 500 on the left. The grid contains the following bits:

1	0	1	0	1	0	1	0	1	0
0	0	0	1	0	0	1	1	1	1
1	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	1
1	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	1
1	0	0	0	0	1	1	1	0	1
0	0	0	0	0	0	0	0	0	1
1	0	0	0	0	0	0	0	1	1
0	1	1	1	1	0	0	0	0	1

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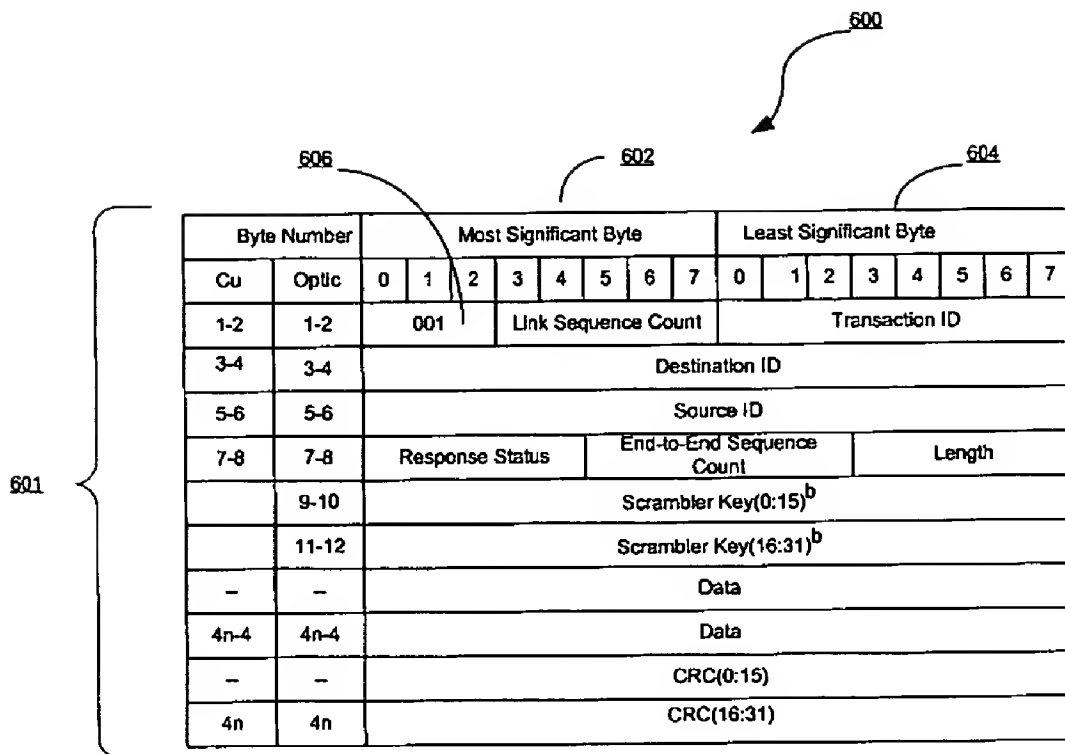


Figure 6

700 702

700

Cycle: 2060254

Command: Link Acknowledgment
Request Link Sequence Count: 00
Ping: 0
Rq B7: 0
Rs B7: 0
Response Link Sequence Count: 16
Suspend: 0
Reset: 0
Heartbeat: 0
Speed Change: 0
Byte 0: A0
Byte 1: 00
Byte 2: 16
Byte 3: 00
CRC Byte 1: 46
CRC Byte 2: 77
CRC Byte 3: A0
CRC Byte 4: A6

Figure 7

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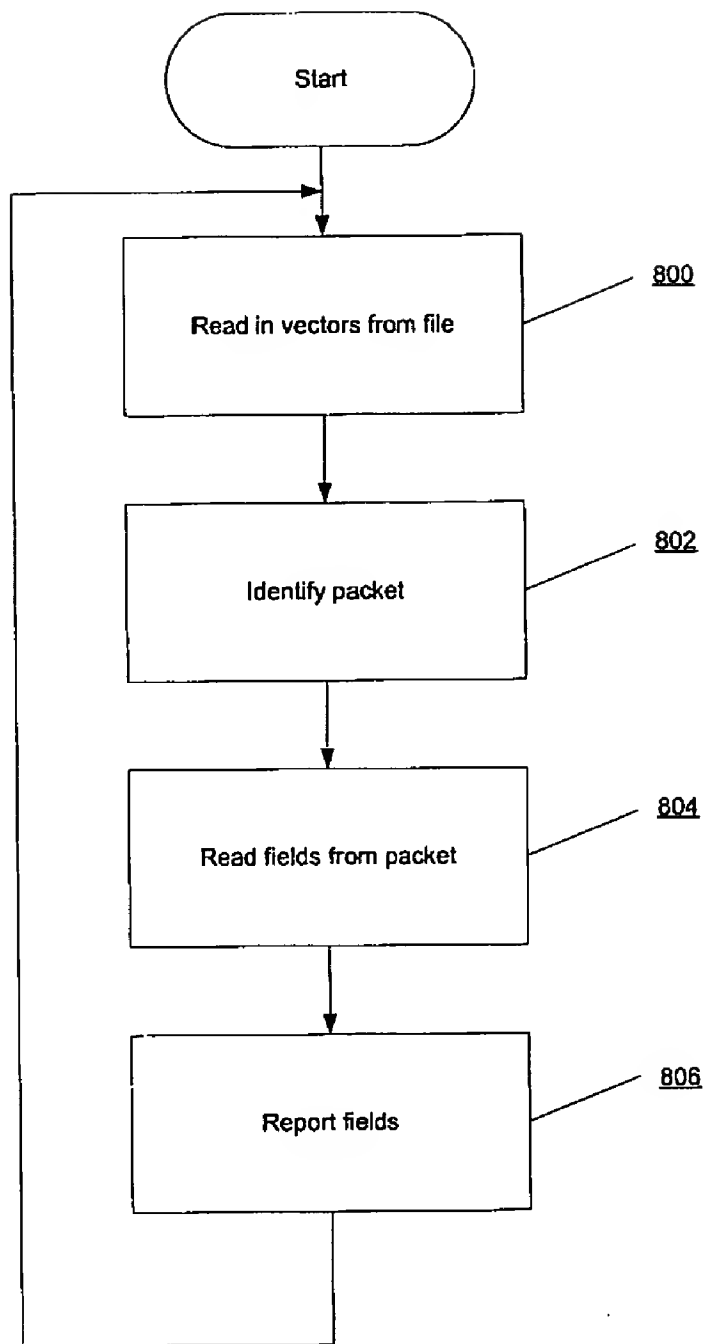


Figure 8

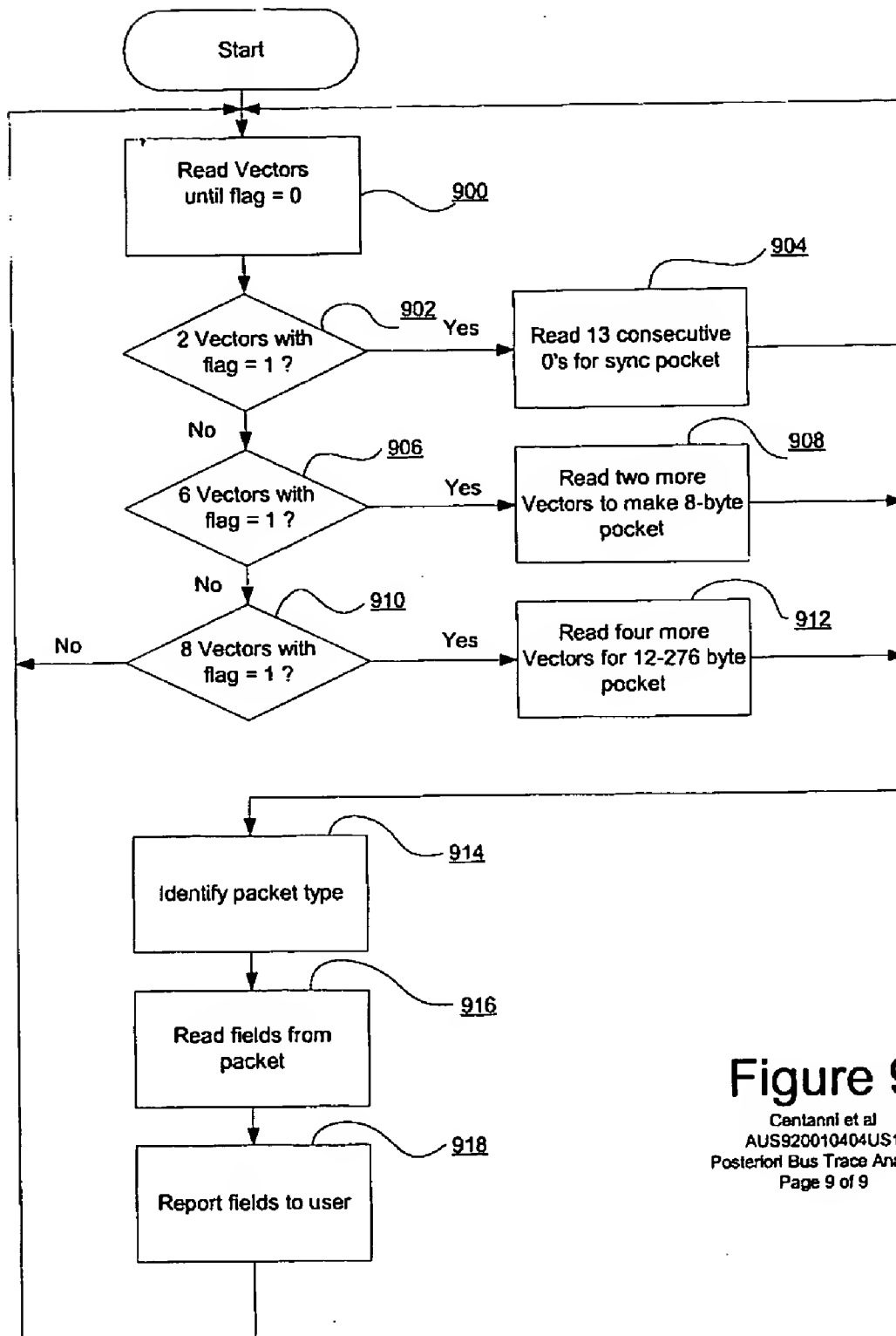


Figure 9